Glossary

Detector nomenclature

Module (M)	one unit of TRD (readout chamber plus radiator)
Layer (L)	5 $\times$ M in longitudinal direction
Stack (S)	6 $\times$ M in radial direction
Supermodule (SM)	5 $\times$ S in longitudinal direction
	6 $\times$ L in radial direction
Plane (P)	one layer in full azimuth, P=18 $\times$ 5$\times$M

Acronyms

A

ADC	Analog to Digital Converter
ALTRO	ALICE TPC Readout (digital chip)
ALU	Arithmetic Lookup Unit

B

BGA	Ball Grid Array

C

CAN	Controller Area Network
CMOS	Complementary Metal-Oxide-Semiconductor
COG	Center Of Gravity
CPU	Central Processing Unit
CTP	Central Trigger Processor

D

DAC	Digital to Analog Converter
DAQ	Data Aquisition
DAQC	Data Acquisition Control
DC	Drift Chamber (also used for Direct Current)
DCOM	Distributed Component Object Model
DCS	Detector Control System
DRAM	Dynamic Random Access Memory

E

ENC	Equivalent Noise Charge
EOS	Electrical Over-Stress
ESD	Electrostatic Discharge

F

FADC	Flash Analog to Digital Converter
FEE	Front End Electronics
FIFO	First In First Out
FF	Flip-Flop
FPC	Flat Printed Circuit
FPGA	Field Programmable Gate Array
FWHM	Full Width Half Maximum

G

GND	Ground
GRF	Global Register File
GTU	Global Tracking Unit

H

HBM	Human Body Model
HLT	High Level Trigger
HTTP	Hyper Text Transfer Protocol
HMPID	High Momentum Particle Identification Detector
HV	High Voltage

I

I2C	Inter-IC
IP	Internet Protocol
ITS	Inner Tracking System

J

JCOP	Joint Control Project
JTAG	Joint Test Action Group

L

L0	Level-0 (trigger)
L1	Level-1 (trigger)
L1A	Level-1 Accept
L1R	Level-1 Reject
L2	Level-2 (trigger)
L2A	Level-2 Accept
L2R	Level-2 Reject
LHC	Large Hadron Collider
LN_2	Liquid Nitrogen
LSB	Least Significant Bit
LTU	Local Tracking Unit
LUT	Look-Up Table
LV	Low Voltage
LVDS	Low Voltage Differential Signal

M

MBS	Multi Branch System (DAQ)
MCM	Multi Chip Module
MIMD	Multiple Instruction Multiple Data
MIP	Minimum Ionizing Particle
MIPS	Mega Instructions Per Second
MM	Machine Model
MMI	Man-Machine Interface
MSB	Most Significant Bit
MWPC	Multi-Wire Proportional Chamber

N

NMOS	Negative Channel Metal-Oxide-Semiconductor

O

OE	Output Enable
OLE	Object Linking and Embeding
OPC	OLE for Process Control

P

PASA	Preamplifier/shaper
PCI	Peripheral Component Interconnect
PHOS	Photon Spectrometer
PID	Particle Identification
PLC	Programable Logic Controller
PLL	Phase Locked Loop
PMOS	Positive Channel Metal-Oxide-Semiconductor
PRF	Pad Response Function
pRF	private Register File

Q

QGP	Quark-Gluon Plasma

R

RAM	Random Access Memory
RMS	Root Mean Square
ROM	Read-Only Memory

S

SCADA	Supervisory Control And Data Acquisition
SIMM	Single Inline Memory Module
SMT	Surface-Mount Technology
SRAM	Static RAM
S/N	Signal-to-Noise ratio

T

TMU	Track-Matching Unit
TOF	Time-Of-Flight (Detector)
TP	Tracklet Processor
TPC	Time Projection Chamber
TPP	Tracklet Preprocessor
TR	Transition Radiation
TRC	Trigger Control
TRD	Transition Radiation Detector
TRF	Time Response Function
TTC	Timing, Trigger and Control

V

VDD	Power Supply for Digital part of FEE
VDDA	Power Supply for Analog part of FEE

W

WE	Write Enable